

PATENT NUMBER and ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM 10014680	FILING DATE 12/11/2001	CLASS 327	SUBCLASS 175	GAU 28% 2881	LU.	XAMINER U	Se: MEMORY
**APPLICANTS: Chawshe Vijay;							
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Verified and Acknowledged Examiners's initials					l	00 000 1191	
TITLE: Circuit and method for adjusting duty cycle of a high speed, low swing output signal							
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TERMINAL	PREPARED FOR ISSUE	Application Examiner			
	Primary Examiner				
Amount Due Date Paid		Sheets Drwg. Figs Drwg. Print Fig.			
ISSUE FEE	CONTRACTOR PROPERTY	的复数 DRAWING 经实际管			
	Assistant Examiner	Total Claims Print Claim for O.G			
NOTICE OF ALLOWANCE MAILED	AN T CIN	CLAIMS ALLOWED			